

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Jingkuang CHEN et al.

Application No.: 10/727,692

Filed: December 4, 2003

Docket No.: 111517.01

For: SYSTEMS AND METHODS FOR INTEGRATION OF HETEROGENEROUS CIRCUIT
DEVICES

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

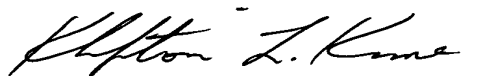
Sir:

Pursuant to 37 CFR §1.56, the attention of the Patent and Trademark Office is hereby directed to the reference(s) listed on the attached PTO-1449. Unless otherwise indicated herein, one copy of each reference is attached. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the reference(s) be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

- ☒ 1. This Information Disclosure Statement is being filed (a) within three months of the U.S. filing date of this non-CPA application, OR (b) before the mailing date of a first Office Action on the merits in the present application. No certification or fee is required.
- ☒ 2. The present application was filed or entered the U.S. National Stage of the PCT after June 30, 2003. In accordance with the June 11, 2003, Notice waiving the requirements of 37 C.F.R. §1.98(a)(2)(i), copies of any U.S. patents and patent application publications are not attached.

- ☒ 3. The references were cited by or submitted to the Office in parent application No. 09/683,857, filed February 22, 2002, which is relied upon for an earlier filing date under 35 U.S.C. §120. Thus, copies of these references are not attached. 37 CFR §1.98(d).

Respectfully submitted,



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JAO:KLK/kzb

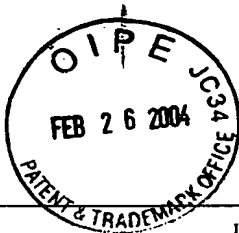
Date: February 26, 2004

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Sheet 1 of 1

Form PTO-1449 (REV. 8-83)		US Dept. of Commerce PATENT & TRADEMARK OFFICE		ATTY DOCKET NO. 111517.01		APPLICATION NO. 10/727,692	
INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)				APPLICANT(S) Jingkuwang CHEN et al.			
				FILING DATE December 4, 2003			
U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	
		5,242,841	09-1993	Smayling et al.	438	275	
		5,519,247	05-1996	Arbus et al.	257	437	
		6,444,487	09-2002	Boggs et al.	438	48	
		6,130,458	10-2000	Takagi et al.	257	351	
FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)							
		Quirk et al., Semiconductor Manufacturing Technology (2001), Prentice Hall, pages 228, 229 and 348					
EXAMINER				DATE CONSIDERED			
Examiner: Initial if citation considered, whether or not citation is in conformance with M.P.E.P. 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							

Date: February 26, 2004